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In the United States Patent and Trademark Office

Serial Number: 10/772,053
Appn. Filed: 02/03/04
Applicant: Guobiao Zhang
Appn. Title: Three-Dimensional Memory-Based Self-Test
Integrated Circuits and Methods
Examiner/GAU: 2814

Mailed: September 23, 2005

Information Disclosure Statement

Assistant Commissioner for Patents
Alexandria, VA 22313-1540

Sir:

Attached is a completed Form PTO-1449 and copies of the pertinent parts of the references cited thereon.

Very respectfully,

Guobiao Zhang
Applicant Pro Se

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ENC: PTO-1449 & References



Sheet 1 of 1

FORM PTO-1449 (REV. 7-80)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. GB8I		SERIAL NO. 10/772,053	
LIST OF PRIOR ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Guobiao Zhang			
				FILING DATE 02/03/04		GROUP 2814	
U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA	5,130,645	07/14/92	Levy	324	158	
	AB	5,239,262	08/24/93	Grutzner et al.	324	158	
	AC	5,592,616	01/07/97	Finch et al.	395	183.18	
	AD	5,835,396	11/10/98	Zhang	365	51	
	AE	6,034,882	03/07/00	Johnson et al.	365	103	
OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
EXAMINER				DATE CONSIDERED			
<small>* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</small>							